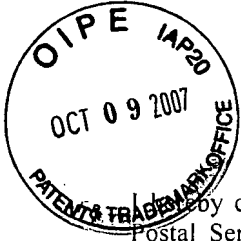


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CFC



PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Oct. 4, 2007
Date

Beverly Loken
Beverly Loken

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ole Bentz

Attorney Docket No.: 500844.01

Patent No. : US 6,975,320 B1

Issued : December 13, 2005

Title : METHOD AND APPARATUS FOR LEVEL-OF-DETAIL COMPUTATIONS

NOTIFICATION OF ERRORS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
NOV 09 2007
of Correction

Sir:

The following errors were noted in a review of the above-identified letters patent. One or more of these errors was inadvertently made in the original application, while the others occurred in the printing of the patent. Since the errors are of an obvious nature, a formal Certificate of Correction is not believed to be warranted at this time. Therefore, applicant requests that this notification be placed in the Patent and Trademark Office file.

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Item (57), Line 2	"(LOD) value for the of texels of a texture map"	--(LOD) value for the texels of a texture map--
Column 1, Line 52	"two texel step, than the"	--two texel step, then the--
Column 1, Line 66	"with predetermined locked to values"	--with predetermined locked values--

Column 2, Line 19	"computing a level-of detail (LOD) value for the of texels"	--computing a level-of-detail (LOD) value for the texels--
Column 2, Line 22	"texels for one pixels,"	--texels for one pixel,--
Column 2, Line 67	"operator or an"	--operator of an--
Column 3, Line 65	"and from an local memory"	--and from a local memory--
Column 3, Line 66	"color values aid"	--color values and--
Column 6, Line 4	"and defines as the value"	--and defined as the value--
Column 6, Line 41	"is 32-bit unsigned"	--is a 32-bit unsigned--
Column 6, Line 54	"LOD values is"	--LOD value is--
Column 7, Line 57	"where number of integer"	--where the number of integer--
Column 9, Line 48	"processing system, comprising:"	--processing system comprising:--

Respectfully submitted,

Date:

Oct. 1, 2007

By:



Edward W. Bulchis, Reg. No. 26,847

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Enclosure:

Postcard